Cyber-Physical Systems and Discrete Controllers CS684: Embedded Systems Topic 1

Paritosh Pandya

Indian Institute of Technology, Bombay

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Cyber Physical System

- Physical system consisting of mechanical and electronics components controlled by computerised controller.
- Languages for Programming Discrete Controllers: C, SystemC, Verilog/VHDL, State charts, Simulink/Stateflow, Lustre/SCADE/Heptagon.

Synchronous Dataflow Programming

- Using language Lustre/Heptagon
- \bullet Compiler: Lustre/Heptagon \rightarrow C/Verilog
- Why Lustre/Heptagon?
 - Very high level modelling language for Discrete Control
 - Functional Dataflow Programs
 - Logical concurrency with clean semantics
 - Determinsitic execution
 - Automatic generation of Efficient C code



- Behavior of the temperature in the tank
 - When the gas burner is OFF the temperature evolves according to x(t) = I e^{-Kt}
 i.e. x⁻ = -Kx



Fig. 1. Our running example

 When the gas burner is ON the temperature evolves according to x(t) = I e^{-Kt} + h (I-e^{-Kt}) i.e. x⁻ = K(h-x)

Where I is the initial temperature of the water, K is a constant that depends on the nature of the tank (how much it conducts heat for example), h is a constant that depends on the power of the gas burner, and t models time.

• We will refer to ON and OFF as modes of the tank evolution.

Model of Cyber-Physical System



- Includes physical device and environment
- Continuous Dynamics
 Input U, Output Y and State of the system X.
 Continuous trajectory X : ℜ₀ → Val_X
- Mathamtical Model using Differntial Equations

 $\begin{array}{rcl} X' &=& G(X,U) \\ Y &=& E(X,U) \end{array}$



- Converts a contiuous time physical signal and gives its value to the discrete controller at discrete time points (sampling points).
- Example Analog to Digital Converter (ADC). Organized as Input Driver Library of API calls.
- Conversion and data transfer is done at discrete time points (sampling points) decided by the supervisor.
- General mathematical model of Interface: Combined Differential+Difference equations.



- Converts a discrete time signal from controller into contiuous physical signal tp the plant.
- Example Digital to Analog Converter (DAC). Some outputs may require further computation provided by output drive library.
- Conversion and data transfer is done at discrete time points (actuation points) decided by the supervisor.
- General model of Interace: Combined Differential+Difference equations.

- Invoked by supervisor repeatedly.
- At each invocation supervisor provides input value.
- Discrete controller produces coresponding output value.
- current output can depend on the sequence of past inputs.
- Example node MINSOFAR(I : int) returns (0 : int) returns minimum of inputs seen so far.

1	4	0	2	-3
0	4	0	0	-3
clock	0	1	2	3

Discrete Conctroller Input Output



- Works in discrete steps of computation.
- Realised using computational devices like Microcontroller, or FPGA, or Sequential Circuit with Flipflops and Gates.
- Inputs *I*, Outputs *O* and State *Z*.
- Time is Discrete, i.e. N. A time points is called a clock cycle, tick, or reaction. Discrete flow: *I* : N → Val_I

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Model (Forward): Difference Equation / Recurrence

$$Z_0 = R(L)$$

$$Z^+ = F(Z, I)$$

$$O = H(Z, I) \leftarrow ($$

 $= H(2_0)$

Discrete Controller

Model (backward): Difference Equation / Recurrence.

 $Z_0 = R(I)$ $Z = F(Z^-, I)$ O = H(Z, I)

Discrete Controller



2	=	$F(Z^{-}, I)$	
0	=	H(Z, I)	

Behaviour of controller

1	<i>I</i> ₀	I_1	I_2	
Ζ-	—	Z_0	Z	
Ζ	Z_0	Z_1	Z_2	
0	O_0	O_1	<i>O</i> ₂	
clocktick	0	1	2	

A discrete controller which at each step returns the minimum of input values seen so far.

node MINSOFAR(I:int) returns (0:int)
var m:int;

 $m_0 = l_0$ $m_i = min(m_{i-1}, l_i)$ $o_i = m_i$

Behaviour

1	4	0	2	-3
М	4	0	0	-3
0	4	0	0	-3
tick	0	1	2	3

Event and Time Triggered Systems

InitializeControllerMemory;	<pre>InitializeControllerMemory;</pre>
for each input event do	for each clock tick do
Read Inputs;	Read Inputs;
Controller step{	Controller step{
<compute outputs=""></compute>	<compute outputs=""></compute>
<update controllermemory="">}</update>	<update controllermemory="">}</update>
Perform Actuation	Perform Actuation
end	end

Reactive Kernel Input Output



- Definition of Controller Memory data type Controller_f_mem Definition of Structure defining Output data type Controller_f_out
- Declaration of controller Memory variable mem :Controller__f_mem
- **Reset function** Procedure resetting controller to initial state Z₀. void Controller_f_reset(Controller_f_mem* self)
- Step function Procedure computing next memory state + 1 and current output O. VS 2 21 void Controller_f_step(t1 x1 , ..., tn xn , Controller_f_out* _out, Controller_f_mem* self);

```
#include "controller.h"
int main(int argc, char * argv[]) {
/* declare state variables */
  Controller__f_m mem;
/* declare input variable */
  t1 x1 ;
  . . .
  tn xn ;
/* declare output variables */
  Controller__f_out ans:
```

```
/* initialize memory instance */
   Controller__f_reset(&mem);
/* repeatedly perform reaction cycle */
   while(1) {
      /* read inputs */
      scanf("..., &x1 , ..., &xn );
      /* perform step */
      Controller_f_step(x1 , ..., xn , &ans, &mem);
      /* write outputs */
      printf("...", ans.y1 , ..., ans.yp );
      }
```

- Cyber physical system consists of physical elements controlled by the conglomorate of Supervisor, Discrete Controller and the Input-output Interfaces.
- For interacting with physical devices, the supervisor uses API calls from Input Interface Drivers and Output Interface Drivers.
- Control is achived by repeated execution of Sense, ComputeStep, Actuate cycle as directed by the supervisor.
- A Discrete Controller transforms an input flow into an output flow, synchronously.
- A discrete controller can be given as reactive kernel consisting of a reset and a step function with associated declarations.

- Examples Nuclear reactors, Air Craft Controller, Space Ships (Launch Vehicles, Landers), Defense equipment,yTransportation systems, Medical equipment, Smart ...
- Safety Critical and Mission critical Cost of failure high

Challanges

- Low productivity. High development costs. High time-to-market.
- Certification Software and its development lifecycle DO-178B (Aircrafts)
- Validation and Verification

- Build a high level model of System (Discrete Controller) Behaviour
- Executable: Deterministic, concurrent, modular, hierarchical.
- Validation and Verification:
 - Simulation
 - Testing: Requirement based, Coverage.
 - Formal Verification
- Automatic code generation: certified.

Languages for Model Based Design

State Charts, Simulink+State-flow, Verilog, Lustre/Scade/Heptagon